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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/052,113	01/17/2002	Ming-Hsien Lee	02106-URLX	1642
33804 75	90 04/19/2005		EXAMINER	
SUPREME PATENT SERVICES			FLANAGAN, KRISTA M	
POST OFFICE BOX 2339 SARATOGA, CA 95070			ART UNIT	PAPER NUMBER
			2631	
			DATE MAILED: 04/19/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/052,113	LEE ET AL.				
Office Action Summary	Examiner	Art Unit				
	Krista M. Flanagan	2631				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply if NO period for reply is specified above, the maximum statutory period was preply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be timed within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE!	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status	,					
1)⊠ Responsive to communication(s) filed on <u>17 January 2002</u> .						
2a) This action is <b>FINAL</b> . 2b) ⊠ This	action is non-final.					
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ☐ Claim(s) 1-11 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-11 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	vn from consideration.					
Application Papers						
9) ☐ The specification is objected to by the Examiner 10) ☑ The drawing(s) filed on 17 January 2002 is/are:  Applicant may not request that any objection to the concept that the correction of the concept that the concept that the correction of the concept that the concept t	a) accepted or b) ⊠ objected drawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	ected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119		•				
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in Application ity documents have been received (PCT Rule 17.2(a)).	on No d in this National Stage				
Attachment(s)						
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)         Paper No(s)/Mail Date     </li> </ol>	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:					

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#### DETAILED ACTION

## **Drawings**

1. Figures 1 and 2 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the examiner does not accept the changes, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

# Claim Objections

2. Claims 2, 6, and 9 are objected to because of the following informalities: Regarding each of the claims, It is the opinion of the examiner that the statement "wherein ... each comprises a D flip-flop..." should be changed to "wherein ... each comprise a D flip-flop..." Appropriate correction is required.

#### Claim Rejections - 35 USC § 112

- 3. The following is a quotation of the second paragraph of 35 U.S.C. 112:
  The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter, which the applicant regards as his invention.
- 4. Claims 1, 5, and 8 are rejected under 35 U.S.C. 112, second paragraph, as being incomplete for omitting essential structural cooperative relationships of elements, such omission amounting to a gap between the necessary structural connections. See MPEP § 2172.01. The omitted structural cooperative relationships are: between the first circuit and the second circuit paths.

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# Claim Rejections - 35 USC § 102

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5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 6. Claims 1 and 2 are rejected under 35 U.S.C. 102(b) as being anticipated by Wade et al US Patent No. 5,359,630.
- 7. Regarding claim 1, Wade discloses a method and apparatus for realignment of synchronous data that generates aligned clock and data signals. Wade discloses a circuit for generating aligned clock and data signals (See column 4, lines 17-22 and figure 1), comprising: a clock input (see figure 1, "xmt clk 100"); a first circuit path receiving an external clock signal from said clock input, and generating an aligned clock signal (See figure 1, "fwd clk 102"); and a second circuit path generating an aligned data signal (See figure 1, "data in 101"); wherein said first and second circuit paths comprise identical circuit components (See figure 1 where comprise is an open term).
- 8. Regarding claim 2, which inherits all of the limitations of claim 1, Wade discloses wherein said first and second circuit paths each comprise a D flip-flop and an output buffer (See figure 1, blocks 8 and 9 and figure 2, blocks 20 and 30).

## Allowable Subject Matter

9. Claims 3-4, 6, 7, and 9-11 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

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### Conclusion

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10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- a. US Patent No. 5,115,455 to Samaras et al. discloses a method and apparatus for stabilized data transmission.
- b. US Patent No. 6,738,417 to Kim et al. discloses a method and apparatus for bidirectional data transfer between a digital display and a computer.
- c. US Patent No. 6751743 to Theodoras et al. discloses a method and apparatus for selecting a first clock and a second clock for first and second devices respectively from an up-converted clock and an aligned clock for synchronization.
- 11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Krista M. Flanagan whose telephone number is (571) 272-2203. The examiner can normally be reached on Monday Friday, 8 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad H. Ghayour can be reached on (571) 272-3021. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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